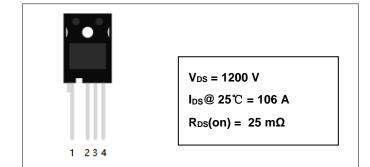


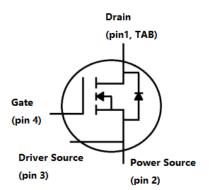
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# S3M0025120K 1200V SIC POWER MOSFET



### **Circuit Diagram**



#### Description

S3M0025120K is single SiC Power MOSFET packaged in TO-247-4 case. The device is a high voltage n-channel Enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S3M0025120K is ideal for energy sensitive, high frequency applications in challenging environments.

#### Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = 25 mΩ.
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin.
- "-A" is an AEC-Q101 qualified device.

#### Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

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### Maximum Ratings (T<sub>A</sub> = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units	Note
Drain - Source Voltage	VDSmax	$V_{GS} = 0 V, I_D = 100 \ \mu A$			1200	V	
Gate - Source Voltage (dynamic)	VGSmax	AC (f > 1 Hz)	-8		+22	V	
Gate - Source Voltage (static)	V <sub>GSop</sub>	Static		-4 / +18		V	[1]
		V <sub>GS</sub> = 18 V, T <sub>C</sub> = 25 °C			106	•	
Continuous Drain Current	ID	$V_{GS}$ = 18 V, $T_{C}$ = 100 °C			75	A	
Pulsed Drain Current	I <sub>D(pulse)</sub>	Pulse width t <sub>P</sub> limited by T <sub>jmax</sub>			200	А	
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C			517	W	

[1] Recommended turn off gate voltage is -4 V. Recommended turn on gate voltage is 18 V. Do not use with V<sub>GSON</sub> < 12 V.

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## Electrical Characteristics (T<sub>A</sub> = 25 °C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Units
Drain Source Breakdown Voltage	V(BR)DSS	$V_{GS}=0~V,~I_{D}=100~\mu A$	1200			V
Coto Throshold Voltage	Maaria	$V_{DS} = V_{GS}, I_D = 20 \text{ mA}$	2	2.5	4	V
Gate Threshold Voltage	V <sub>GS</sub> (th)	$V_{DS}=V_{GS},I_{D}=20\text{ mA},T_{J}=175~\circ C$		1.6		V
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V		1	100	μΑ
Gate Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = 18 \text{ V},  V_{DS} = 0 \text{ V}$		10	250	nA
Drain Source On-State	5	$V_{GS} = 18 \text{ V}, I_D = 48 \text{ A}$		25	32	mΩ
Resistance	RDS(on)	R <sub>DS(on)</sub> V <sub>GS</sub> = 18 V, I <sub>D</sub> = 48 A, T <sub>J</sub> = 175 °C		36		mΩ
		V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 48 A		28		S
Transconductance	gfs V <sub>DS</sub> =	$V_{DS} = 20 \text{ V}, \text{ I}_{DS} = 48 \text{ A}, \text{ T}_{J} = 175 ^{\circ}\text{C}$		27		S
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V		3519		
Output Capacitance	Coss	V <sub>DS</sub> = 1000 V		151		pF
Reverse Transfer Capacitance	Crss	V <sub>AC</sub> = 25 mV		19		
Coss Stored Energy	Eoss	f = 1 MHz		91		μJ
Turn-On Switching Energy	Eon	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -4 / 18 V		260		
Turn-Off Switching Energy	EOFF	I <sub>D</sub> = 48 A, R <sub>G(ext)</sub> = 2.5 Ω, L = 99 uH		231		μJ
Turn-On Delay Time	t <sub>d(on)</sub>	(on) V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -4 / 18 V 13		13		
Rise Time	tr	$I_{D} = 48 \text{ A}, R_{G(ext)} = 2.5 \Omega$		16		ns

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Turn-Off Delay Time	$t_{d(\text{off})}$	Inductive Load Timing relative to	33		
Fall Time	t <sub>f</sub>	VDS Per IEC60747-8-4 pg 83	8		
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1 MHz, AC = 25 mV	1.6		Ω
Gate to Source Charge	Qgs	$V_{DS} = 800 \text{ V}, \text{ V}_{GS} = -4 / 18 \text{ V}$	80		
Gate to Drain Charge	$Q_gd$	I <sub>D</sub> = 48 A	41		nC
Total Gate Charge	Qg	Per IEC60747-8-4 pg 21	175		

#### **Reverse Diode Characteristics (T<sub>A</sub> = 25 °C, unless otherwise specified)**

Characteristics	Symbol	Conditions	Тур.	Max.	Units
Diode Forward Voltage	$V_{SD}$	V <sub>GS</sub> = -4 V, I <sub>SD</sub> = 24 A	5.2		V
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -4 V, I <sub>SD</sub> = 24 A, T <sub>J</sub> = 175°C	4.5		V
Continuous Diode Forward Current	Is	V <sub>GS</sub> = -4 V, T <sub>C</sub> = 25 °C	121		А
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS}$ = -4 V, I <sub>SD</sub> = 48 A, T <sub>J</sub> = 25 °C	18		ns
Reverse Recovery Charge	Qrr	V <sub>R</sub> = 800V	260		nC
Peak Reverse Recovery Current	I <sub>mm</sub>	dif / dt = 2500 A / µs	21		А

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#### **Thermal-Mechanical Specifications**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R <sub>θJC</sub>	DC operation	0.29	°C/W

## **Ordering Information**

Device	Package	Shipping
S3M0025120K	TO-247-4	30pcs/tube

#### Marking Diagram



#### Where XXXXX is YYWWL

S3M 0025 120 K SSG YY WW	= Device Type = R <sub>DS</sub> (on) = Reverse Voltage (1200V) = Package = SSG = Year = Week
L	= Lot Number
utions.	Molding resin

Cautions: Molding resin Epoxy resin UL:94V-0



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#### **Ratings and Characteristics Curves**

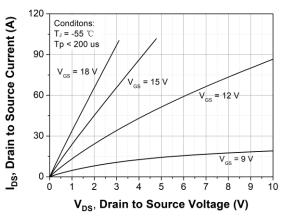


Figure 1. Output Characteristics  $T_J = -55 \ ^{\circ}C$ 

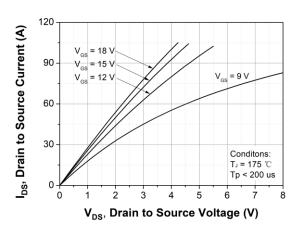


Figure 3. Output Characteristics T<sub>J</sub> = 175 °C

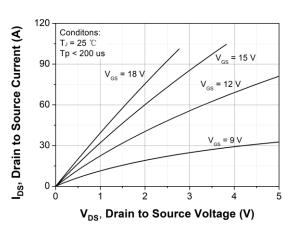


Figure 2. Output Characteristics T<sub>J</sub> = 25 °C

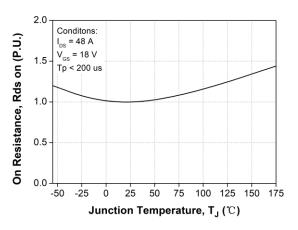
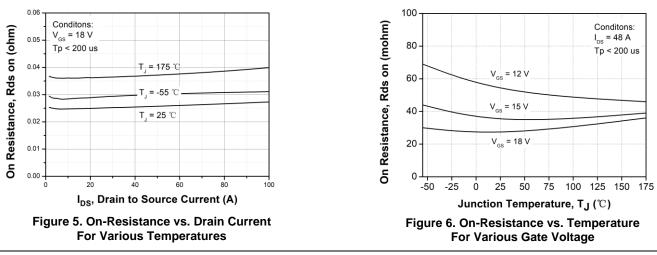


Figure 4. Normalized On-Resistance vs. Temperature



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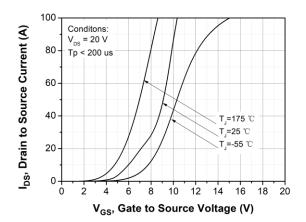


Figure 7. Transfer Characteristic for Various Junction Temperatures

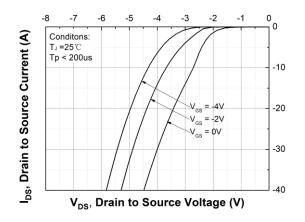


Figure 9. Body Diode Characteristic at T = 25 °C

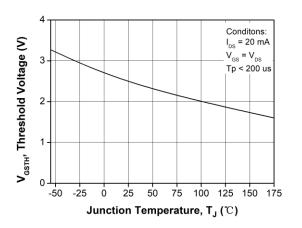


Figure 11. Threshold Voltage vs. Temperature

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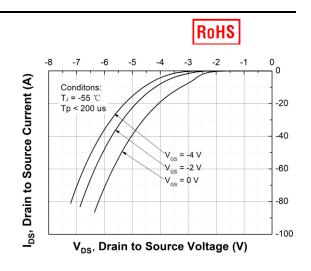


Figure 8. Body Diode Characteristic at  $T_J = -55$  °C

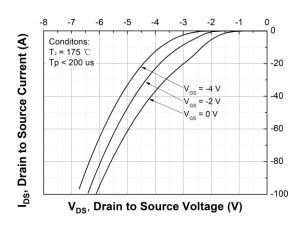


Figure 10. Body Diode Characteristic at T<sub>J</sub> = 175 °C

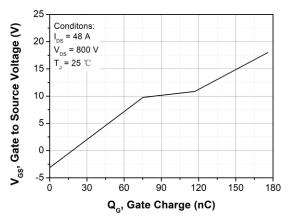


Figure 12. Gate Charge Characteristic

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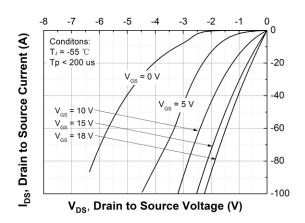


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

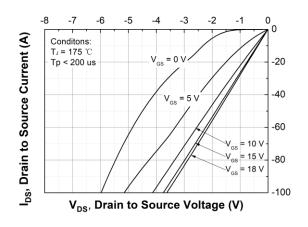
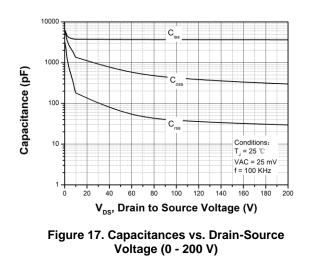
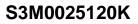


Figure 15. 3rd Quadrant Characteristic at  $T_J = 175 \ ^{\circ}C$ 





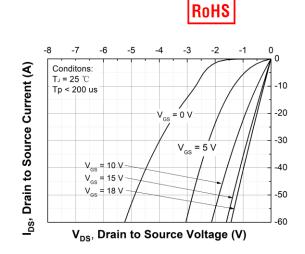


Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

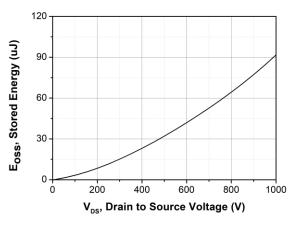


Figure 16. Output Capacitor Stored Energy

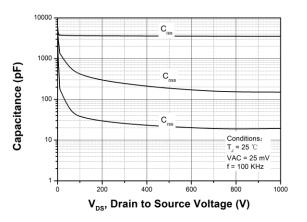


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000 V)

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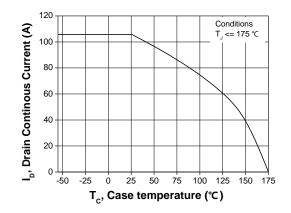


Figure 19. Continuous Drain Current Derating vs. Case Temperature

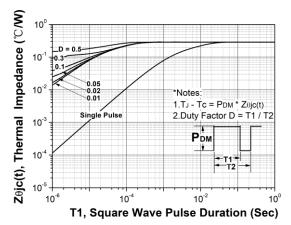


Figure 21. Transient Thermal Impedance (Junction - Case)

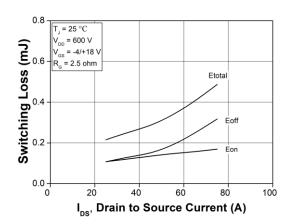


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 600V)

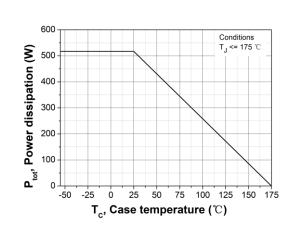


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

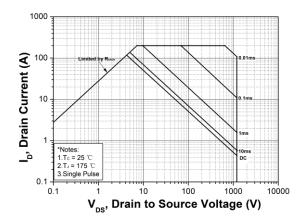


Figure 22. Safe Operating Area

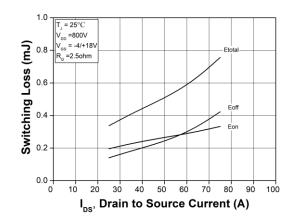


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 800V)

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150

T. = 25°C

<sub>DD</sub> = 800V

= -4/+18\

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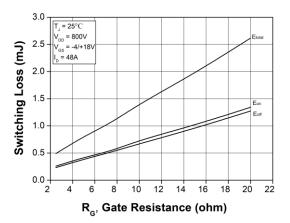


Figure 25. Clamped Inductive Switching Energy vs. R<sub>G(ext)</sub>

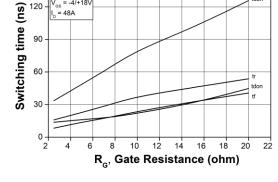


Figure 27. Switching Times vs. R<sub>G(ext)</sub>

0.0 0 25 50 75 100 125 150 175 200  ${\bf T}_{_J},$  Junction Temperature (°C) Figure 26. Clamped Inductive Switching Energy vs. Temperature

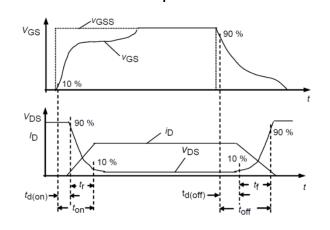
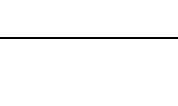


Figure 28. Switching Times Definition



Eoff Eon



tdoff

0.7

0.6

0.5

0.4

0.3

0.2

0.1

Switching Loss (mJ)

R\_ = 2.5ohm

= 48A

= 800V

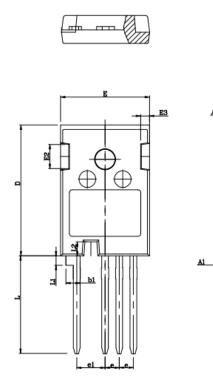
= -4/+18

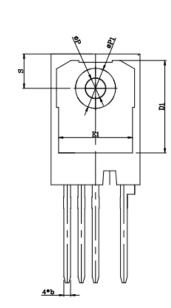


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#### **Mechanical Dimensions TO-247-4**





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SYMBOL	mm				
STMBUL	Min	Nom	Max		
Α	4.80	5.00	5.20		
Al	2.23	2.41	2.59		
A2	1.85	2.00	2.15		
b	1.11	1.21	1.36		
b1	2.35	2.55	2.75		
с	0.51	0.61	0.75		
D	23.30	23.45	23.60		
Dl	16.25	16.55	16.85		
Е	15.75	15.94	16.10		
El	13.00	13.26	13.43		
E2	4.00	4.30	4.60		
E3	1.15	1.45	1.75		
e		2.54BSC			
el		5.08BSC			
L	17.31	17.47	17.82		
Ll	1.50	1.70	1.90		
ØP	3.51	3.60	3.65		
ØP1	7.08	7.19	7.30		
S	6.15BSC				



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